

In Re Patent Application of:  
**COFFA ET AL.**  
Serial No: **Not Yet Assigned**  
Filing Date: **Herewith**

wherein said upper layer comprises an epitaxial silicon layer on said substrate.

47. An integrated circuit according to Claim 40 wherein said electronic circuitry comprises:

a respective analog/digital converter for each pressure sensor; and

a processor connected to said analog/digital converters for performing a time-delay correlation between signals produced by different pressure sensors and generating a value of delay for which the time-delay correlation is maximum, and for calculating a direction of a sound source for a sound wave as sensed by said pressure sensors based upon the time delays.

#### REMARKS

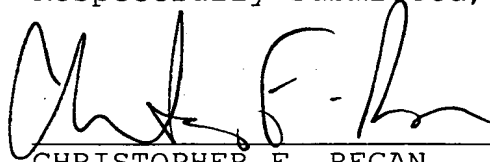
It is believed that all of the claims are patentable over the prior art. For better readability and the Examiner's convenience, the newly submitted claims differ from the translated counterpart claims which are being canceled. The newly submitted claims do not represent changes or amendments that narrow the claim scope for any reason related to the statutory requirements for patentability. Accordingly, after the Examiner completes a thorough examination and finds the claims patentable, a Notice of Allowance is respectfully requested in due course. Should the Examiner determine any minor informalities that need to be addressed, he is

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encouraged to contact the undersigned attorney at the  
telephone number below.

Respectfully submitted,



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